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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takahiro OKUNO, et al.

SERIAL NUMBER: 10/814,246

GROUP: 2811

FILED: April 1, 2004

EXAMINER: ARENA, ANDREW
OWENS

FOR: INJECTION ENHANCED GATE TRANSISTOR INCLUDING SECOND EMITTER
IN DUMMY REGION TO PREVENT WAVEFORM VIBRATION ASSOCIATED
WITH NEGATIVE GATE CAPACITANCE

REQUEST TO CORRECT TITLE OF INVENTION

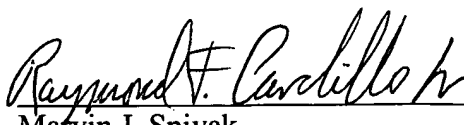
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ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request
correction of your records to reflect the correct title of the invention. The title of the
invention should read as follows: INJECTION ENHANCED GATE TRANSISTOR
INCLUDING SECOND EMITTER IN DUMMY REGION TO PREVENT WAVEFORM
VIBRATION ASSOCIATED WITH NEGATIVE GATE CAPACITANCE.

Respectfully Submitted,

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